/\*\*

\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

\* @file : main.c

\* @author : Auto-generated by STM32CubeIDE

\* @brief : Main program body

\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

\* @attention

\*

\* <h2><center>&copy; Copyright (c) 2021 STMicroelectronics.

\* All rights reserved.</center></h2>

\*

\* This software component is licensed by ST under BSD 3-Clause license,

\* the "License"; You may not use this file except in compliance with the

\* License. You may obtain a copy of the License at:

\* opensource.org/licenses/BSD-3-Clause

\*

\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

\*/

**#include**"STM32F4xx.h"

**#include** <stdint.h>

**#if** !defined(\_\_SOFT\_FP\_\_) && defined(\_\_ARM\_FP)

**#warning** "FPU is not initialized, but the project is compiling for an FPU. Please initialize the FPU before use."

**#endif**

**#define** ADC\_BASE\_ADDR 0x40012000UL

**#define** ADC\_CR1\_OFFSET 0x04UL

**#define** ADC\_CR1\_REG\_ADDR (ADC\_BASE\_ADDR+ADC\_CR1\_OFFSET)

**#define** RCC\_BASE\_ADDR 0x40023800UL

**#define** RCC\_APB2ENR\_OFFSET 0x44UL

**#define** RCC\_APB2ENR\_ADDR (RCC\_BASE\_ADDR+RCC\_APB2ENR\_OFFSET)

**int** **main**(**void**)

{

uint32\_t \*pADcCr1Reg = (uint32\_t\*) ADC\_CR1\_REG\_ADDR;

uint32\_t \*pRccApb2Enr= (uint32\_t\*) RCC\_APB2ENR\_ADDR;

\*pRccApb2Enr|= (1<<8);

\*pADcCr1Reg|= (1<<8);

/\* Loop forever \*/

**for**(;;);

}